	Application No.	Applicant(s)
Notice of Allowability	10/680,397	HIDAKA, HIDETO
	Examiner	Art Unit
	George C. Eckert II	2815
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. 🔀 This communication is responsive to applicant's divisional application.		
2. The allowed claim(s) is/are 31-34.		
3. 🔀 The drawings filed on <u>08 October 2003</u> are accepted by the Examiner.		
 4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a)		
 Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 10/8/03, 3/17/04 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material 	6. ☐ Interview Summary Paper No./Mail Da 98), 7. ⊠ Examiner's Amendr	te

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EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or

additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR

1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the

payment of the issue fee.

The application has been amended as follows: in the claims:

Claim 31, line 14; delete the first occurrence of "circuit" and insert --current-- in its

place.

Allowable Subject Matter

Claims 31-34 are allowed. The following is an examiner's statement of reasons for

allowance: the prior art does not teach or suggest, either alone or in combination, a device as

instantly claimed and comprising a power supply control circuit for setting a gate tunnel current

of an insulated gate field effect transistor of an inactive circuit out of a spare access circuit and a

normal access circuit to be smaller than the gate tunnel current of a transistor in an active circuit

out of the spare and normal access circuits, as instantly claimed and in combination with the

remaining structural elements.

Any comments considered necessary by applicant must be submitted no later than the

payment of the issue fee and, to avoid processing delays, should preferably accompany the issue

fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for

Allowance."

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to George C. Eckert II whose telephone number is (571) 272-1728.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (571) 272-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

GEORGE ECKERT PRIMARY EXAMINER